



05-19-03

GAU 2815

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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	<b>Application Number</b>	09/989,931
	<b>Filing Date</b>	November 21, 2001
	<b>First Named Inventor</b>	Fernando Gonzalez, et al
	<b>Group Art Unit</b>	2815
	<b>Examiner Name</b>	E.J. Wojciechowicz
<b>Total Number of Pages in This Submission</b>		<b>Attorney Docket Number</b> MI22-1801

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
<b>Firm or Individual name</b>	David G. Latwesen, Ph.D.; Wells, St. John, P.S.
<b>Signature</b>	
<b>Date</b>	5/16/03

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EV085439215

Appl. No. 09/989,931



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. ....09/989,931  
Filing Date ..... November 21, 2001  
Inventor ..... Fernando Gonzalez, et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2815  
Examiner ..... Edward Joseph Wojciechowicz  
Attorney's Docket No. ....MI22-1801  
Title: Methods of Forming Semiconductor Circuitry, and Semiconductor Constructions

*#5/election*  
*5/21/03*  
*Smith*

**RESPONSE TO MAY 7, 2003 OFFICE ACTION**

To: Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**AMENDMENTS**

**In the Claims**

None.

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